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- b<sub>1</sub>. further memory means having a plurality of programmable sections;
  - b<sub>2</sub>. externally accessible coupling means connected to said memory means and operable at will temporarily to couple said further memory means with said writing means of said recording means;
  - b<sub>3</sub>. further memory control circuits coupled with said externally accessible coupling means and with said further memory means;
  - b<sub>4</sub>. said further memory means and said further memory control circuits being constructed as logic micro-structures.
3. The invention claimed in claim 2 and further comprising a plurality of writing devices disposed at fixed locations, wherein:
- a. said first memory means includes recording sections operable to store data to be transferred, in a readily portable form;
  - b. said coupling means of said portable electronic device is operable at will temporarily to couple said first recording sections of said first memory means with a said writing device;
  - c. each said writing device comprises writing means operable to enter data into said first memory means of a portable electronic device coupled with said writing device, whereby any person of one of said

18

- populations may receive data from any person of the other of said populations.
4. The invention claimed in claim 2 wherein said memory devices of said portable electronic means are unpowered memory means.
5. The invention claimed in claim 4 wherein said memory means of said portable electronic devices are incorporated in the interiors of said devices in an inaccessible manner.
6. The invention claimed in claim 3 wherein said recording device and a said writing device are interconnected and associated in a single data-transfer device.
7. The invention claimed in claim 2 and wherein:
- a. each of said portable electronic devices includes a second memory means permanently programmed with identification data;
  - b. each said recording and/or writing device includes:
    - b<sub>1</sub>. data entry means operable to introduce identification data into said recording and/or writing device;
    - b<sub>2</sub>. identification comparator means having input arranged to be coupled to said second memory means and to said data entry means; said identification comparator means operating to compare said programmed identification data with said introduced identification data and, when said compared data correspond, to enable said recording and/or writing device.
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